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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

1	Name of the Activity/Event	Guest Lecture on " Memory and programmable logic devices "		
2	Date of Activity/Event	07/02/2022 to 11/02/2022		
3	Organized by	Department of Electronics And Communication Engineering		
4	Place of Activity/event	Visvesvaraya Auditorium		
5	Resource persons / guest / organization	Mr.P. SUDHEER BABU, SDE BSNL, NELLORE		
6	Type of activity/Event	Guest Lecture		
7	Activity/Event objectives	1.This program serves as a platform for students to enable seamless exchange of data between any two points in the world. 2. This exchange of data takes place over a computer network.		
8	Participation	Students	Faculty	Total Participation
		209	-	209
9	General remarks	1. Introduction To logic design. 2. Introduction To memory decoding& flash memory.		
10	Suggested Improvements			
11	Enclosures	1. Program report with Snapshots 2. Attendance sheet		
12	Signature of Co-ordinator			

The Electronics and Communication Engineering department has organized a **Guest Lecture** on "**Memory and programmable logic devices**" from **07/02/2022 to 11/02/2022**, The Resource person is **Mr.P. SUDHEER BABU, SDE BSNL, NELLORE.**

The II B.Tech students from the ECE department had attended this Lecture. Total of **209** students attended to this session.



Resource Person is Explaining about PLA & PLA

In this session resource person covered the syllabus which was related to their curriculum. On the first day he started the session by introducing himself and importance of memory and programmable device.

Later on he discussed some basics which were related to programmable logic devices with respect to syllabus and real time applications.



Explaining about Introduction of Memory and Programmable Logic

Therefore, he explained about the data stored in the ROM is stable, and the stored data doesn't change after power off; the structure is relatively simple, and the reading is convenient, thus it is often used for the task of secondary storage, or long-term persistent storage to store various fixed programs and data. On next day he discussed about PLDs- Programmable Logic Array and Programmable Array Logic.



Explaining about RAM, ROM AND TYPES OF ROM



Explaining about memory decoding and flash memory

Students of II B.Tech actively involved in the session where they recollected the related information and effectively shared their knowledge. Resource Person discussed with real time examples and the session was made really interactive by providing an opportunity. Total 209 students were participated in the session.